Assignment 1

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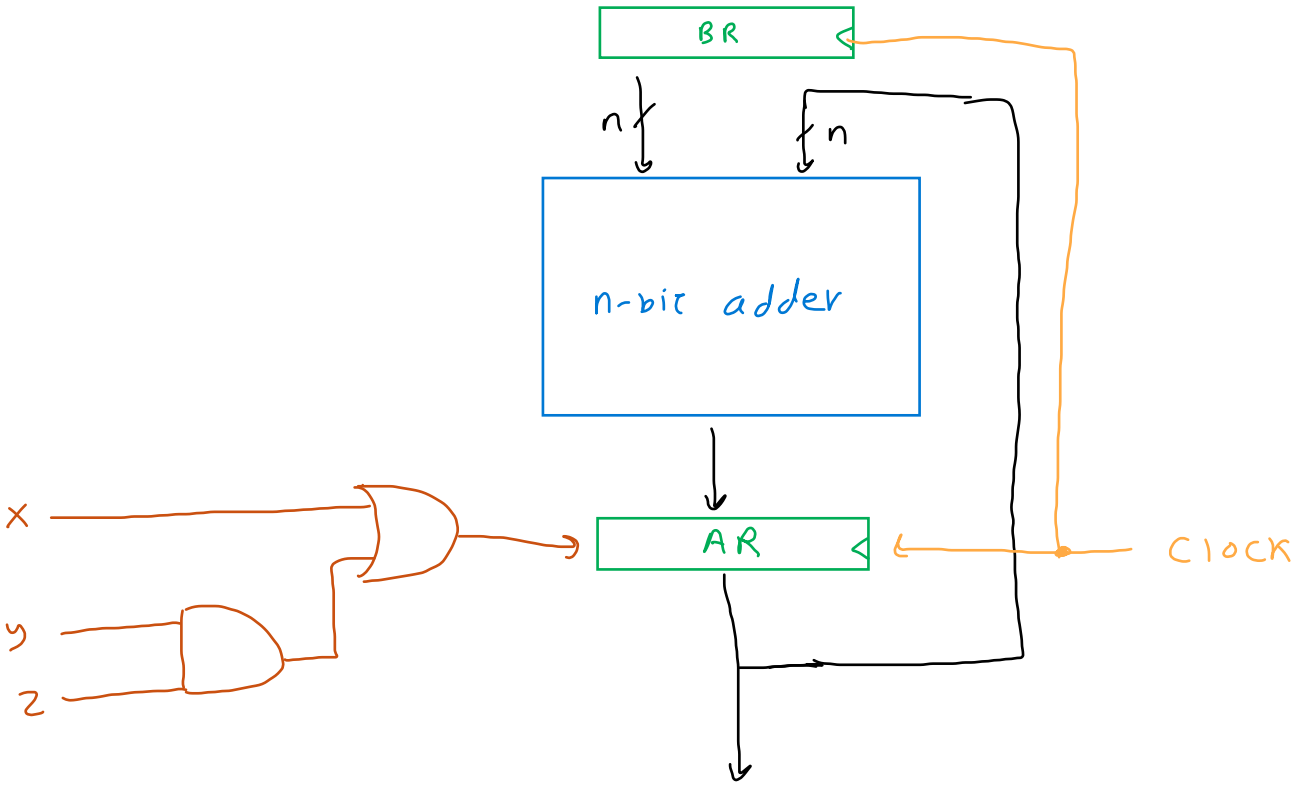
[5] Draw the block diagram for the hardware that implements the following statements:

x + yz: AR ← AR + BR

where AR and BR are two n-bit registers and x, y, and z are control variables. Include the logic gates for the control function.



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