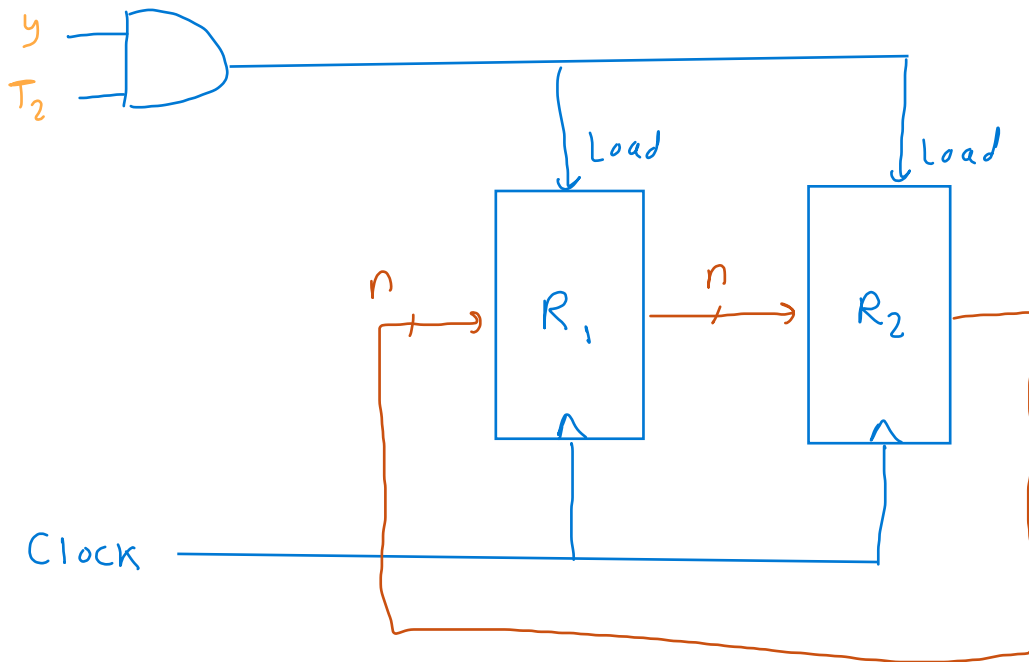


Assignment 1

- [1] Show the block diagram of the hardware that implements the following register transfer statement:

$$yT_2: R_2 \leftarrow R_1, R_1 \leftarrow R_2$$



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